GaAs SPDT Switch DC - 4 GHz

Features

- Terminated (SW-226-PIN), High Isolation (SW-227-PIN), Low Loss (SW-228-PIN)
- Fast Switching Speed: 6 nS Typical
- Ultra Low DC Power Consumption
- Lead-Free 7-Lead Ceramic Package
- RoHS* Compliant and 260°C Reflow Compatible

Description

M/A-COM's SW-226/227/228-PIN are GaAs MMIC SPDT switches packaged in lead-free, surface mount CR-2 ceramic style packages. The SW-226-PIN is a terminated SPDT. The SW-227-PIN offers high isolation. The SW-228-PIN offers low insertion loss. This ceramic switch platform has a common footprint for all three designs. The CR-2 package is hermetically sealed, making these switches ideal for space, military radios, and other environmentally harsh applications.

Typical applications include synthesizer switching, transmit/receive switching, switch matrices and filter banks in systems such as radio and cellular equipment, PCM, GPS, and fiber optic modules.

The SW-226/227/228-PIN are fabricated as monolithic GaAs MMICs using a 1.0 micron MESFET process.

Ordering Information

Part Number	Package
SW-226-PIN	Ceramic (CR-2)
SW-227-PIN	Ceramic (CR-2)
SW-228-PIN	Ceramic (CR-2)

Absolute Maximum Ratings ^{1,2}

Parameter	Absolute Maximum
Input Power 0.05 GHz 0.5 - 4.0 GHz	+27 dBm +34 dBm
Control Voltage	-8.5 V <u><</u> Vc <u><</u> +5 V
Operating Temperature	-55°C to +125°C
Storage Temperature	-65°C to +150°C

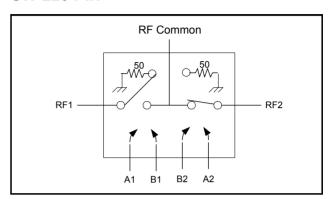
1. Exceeding any one or combination of these limits may cause permanent damage to this device.

2. M/A-COM does not recommend sustained operation near these survivability limits.

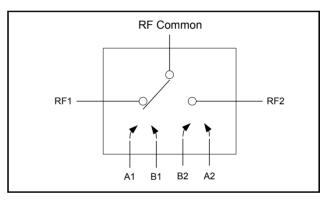
¹ * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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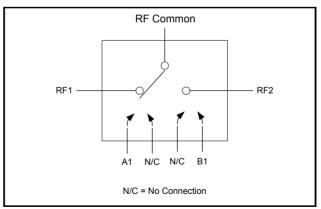
Block Diagram/Pin Configuration SW-226-PIN³



Block Diagram/Pin Configuration SW-227-PIN³



Block Diagram/Pin Configuration SW-228-PIN ³



3. Bottom of case is RF ground.

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Electrical Specifications: $T_A = -55^{\circ}C$ to $+85^{\circ}C$, Vc = 0 V / -5 V, $Z_0 = 50 \Omega^4$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss (SW-226-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	dB dB dB dB			0.9 1.0 1.2 1.5
Insertion Loss (SW-227-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	dB dB dB dB	 	 	0.9 1.0 1.1 1.4
Insertion Loss (SW-228-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	dB dB dB dB			0.7 0.7 0.8 1.0
Isolation (SW-226-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	dB dB dB dB	53 48 40 25		
Isolation (SW-227-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	dB dB dB dB	55 50 40 35		
Isolation (SW-228-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	dB dB dB dB	50 42 32 22		
VSWR (SW-226-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	Ratio Ratio Ratio Ratio			1.2:1 1.4:1 1.6:1 2.3:1
VSWR (SW-227-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	Ratio Ratio Ratio Ratio	 	 	1.2:1 1.4:1 1.6:1 2.0:1
VSWR (SW-228-PIN)	DC - 0.5 GHz DC - 1 GHz DC - 2 GHz DC - 4 GHz	Ratio Ratio Ratio Ratio			1.2:1 1.2:1 1.3:1 1.9:1
Trise, Tfall ^₅	10% to 90% RF, 90% to 10% RF	nS	—	3	—
Ton, Toff ⁵	50% control to 90% RF, 50% control to 10% RF	nS	_	6	_
Transients ⁵ (SW-226-PIN,SW-227-PIN)	In-Band	mV	_	30	_
Transients ⁵ (SW-228-PIN)	In-Band	mV	—	10	—

See MIL-STD-883 for environmental screening options.
Faster switching speed can be achieved with enhanced driver waveform.

²

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Electrical Specifications (continued): $T_A = -55^{\circ}C$ to $+85^{\circ}C$, Vc = 0 V / -5 V, $Z_0 = 50 \Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Input P1dB	0.5 - 4 GHz, 0 / -5 VDC 0.05 GHz, 0 / -5 VDC 0.5 - 4 GHz, 0 / -8 VDC 0.05 GHz, 0 / -8 VDC	dBm dBm dBm dBm	 	27 21 33 26	
IP2	For two-tone input power up to +13 dBm 0.5 - 4 GHz 0.05 GHz	dBm dBm	_	68 62	_
IP3	For two-tone input power up to +13 dBm 0.5 - 4 GHz 0.05 GHz	dBm dBm	_	46 40	_
Control Current	$ \begin{vmatrix} Vc & = 0 \text{ to } 0.2 \text{ V} \\ Vc & = 5 \text{ V} (SW-226-PIN, SW-227-PIN) \\ Vc & = 8 \text{ V} (SW-226-PIN, SW-227-PIN) \\ Vc & = 5 \text{ V} (SW-228-PIN) \\ Vc & = 8 \text{ V} (SW-228-PIN) \\ Vc & = 8 \text{ V} (SW-228-PIN) \\ \end{vmatrix} $	μΑ μΑ μΑ μΑ μΑ			20 — 600 — 300

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

SW-226-PIN and SW-227-PIN Truth Table ^{6,7}

Control Input				Condition RF Commo RF F	on to each
A1	B1	A2	B2	RF1	RF2
1	0	0	1	ON	OFF
0	1	1	0	OFF	ON

SW-228-PIN Truth Table ^{6,7}

Control Input		Condition RF Commo RF F	on to each
A1	B1	RF1	RF2
1	0	ON	OFF
0	1	OFF	ON

6. 0 = 0 V to -0.2 V, 1 = -5 V to -8 V

7. For the SW-227-PIN and SW-228-PIN only, when an RF output is "OFF" it is shorted to case ground.

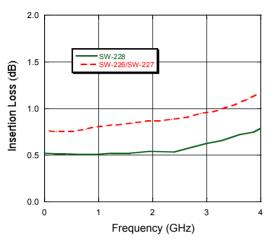
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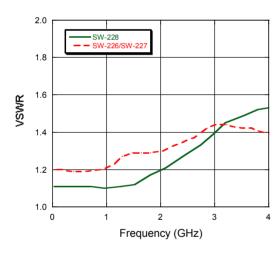
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Typical Performance Curves

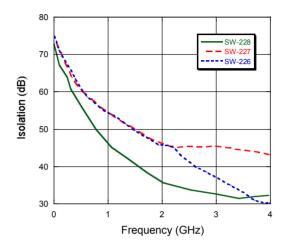
Insertion Loss



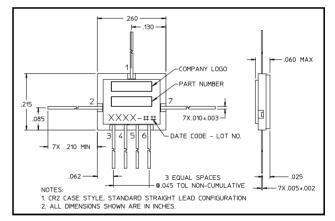




Isolation



Lead-Free CR-2[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.

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